

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Low, *et al.* Docket No.: 02 P 15173 US  
Serial No.: 10/689,233 Art Unit: 2829  
Filed: October 20, 2003 Examiner: Asok K. Sarkar  
For: Inclusion Of Low-K Dielectric Material Between Bit Lines

Mail Stop Amendment  
Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

ELECTION

Dear Sir:

In response to the restriction requirement set forth in the Office Action mailed on January 13, 2005, Applicant hereby elects Group I: Claims 1-48 for further prosecution in the above patent application. Applicant further elects Species I in the event no generic claim is found allowable.

Respectfully submitted,

9 February 2005  
Date

James C. Kesterson  
James C. Kesterson  
Attorney for Applicant  
Reg. No. 25,882

SLATER & MATSIL, L.L.P.  
17950 Preston Rd.  
Suite 1000  
Dallas, TX 75252-5793  
972-732-1001 - Tel  
972-732-9218 - Fax